

**EVLYS LTD. - POWER SEMICONDUCTORS DEVICES -**  
**Wholesale and Retail.**

**Phase Control Disc Thyristor Type DT70-2000-18**

High power cycling capability / Low on-state and switching losses  
 Designed for traction and industrial applications

Mean on-state current	I <sub>TAV</sub>	2000 A			
Repetitive peak off-state voltage	V <sub>DRM</sub>	1000 ÷ 1800 V			
Repetitive peak reverse voltage	V <sub>RRM</sub>				
Turn-off time	t <sub>q</sub>	250 µs			
V <sub>DRM</sub> , V <sub>RRM</sub> , V	1000	1200	1400	1600	1800
Voltage code	10	12	14	16	18
T <sub>j</sub> , °C			– 60 ÷ 125		

**MAXIMUM ALLOWABLE RATINGS**

Symbols and parameters		Units	Values	Test conditions	
<b>ON-STATE</b>					
I <sub>TAV</sub>	Mean on-state current	A	2000 2515 3114	T <sub>c</sub> = 96 °C, Double side cooled T <sub>c</sub> = 85 °C, Double side cooled T <sub>c</sub> = 70°C, Double side cooled 180° half-sine wave; 50 Hz	
I <sub>TRMS</sub>	RMS on-state current	A	3140	T <sub>c</sub> = 96 °C, Double side cooled 180° half-sine wave; 50 Hz	
I <sub>TSM</sub>	Surge on-state current	kA	44.0 51.0	T <sub>j</sub> =T <sub>j</sub> max T <sub>j</sub> =25 °C	180° half-sine wave; 50 Hz (t <sub>p</sub> =10 ms); single pulse; V <sub>D</sub> =V <sub>R</sub> =0 V; Gate pulse: I <sub>G</sub> =2 A; t <sub>GP</sub> =50 µs; dI <sub>G</sub> /dt≥1 A/µs
			47.0 54.0	T <sub>j</sub> =T <sub>j</sub> max T <sub>j</sub> =25 °C	180° half-sine wave; 60 Hz (t <sub>p</sub> =8.3 ms); single pulse; V <sub>D</sub> =V <sub>R</sub> =0 V; Gate pulse: I <sub>G</sub> =2 A; t <sub>GP</sub> =50 µs; dI <sub>G</sub> /dt≥1 A/µs
I <sup>2</sup> t	Safety factor	A <sup>2</sup> s·10 <sup>3</sup>	9680 13005	T <sub>j</sub> =T <sub>j</sub> max T <sub>j</sub> =25 °C	180° half-sine wave; 50 Hz (t <sub>p</sub> =10 ms); single pulse; V <sub>D</sub> =V <sub>R</sub> =0 V; Gate pulse: I <sub>G</sub> =2 A; t <sub>GP</sub> =50 µs; dI <sub>G</sub> /dt≥1 A/µs
			9165 12100	T <sub>j</sub> =T <sub>j</sub> max T <sub>j</sub> =25 °C	180° half-sine wave; 60 Hz (t <sub>p</sub> =8.3 ms); single pulse; V <sub>D</sub> =V <sub>R</sub> =0 V; Gate pulse: I <sub>G</sub> =2 A; t <sub>GP</sub> =50 µs; dI <sub>G</sub> /dt≥1 A/µs
<b>BLOCKING</b>					
V <sub>DRM</sub> , V <sub>RRM</sub>	Repetitive peak off-state and Repetitive peak reverse voltages	V	1000÷1800	T <sub>j min</sub> < T <sub>j</sub> <T <sub>j</sub> max; 180° half-sine wave; 50 Hz; Gate open	
V <sub>DSM</sub> , V <sub>RSM</sub>	Non-repetitive peak off-state and Non-repetitive peak reverse voltages	V	1100÷1900	T <sub>j min</sub> < T <sub>j</sub> <T <sub>j</sub> max; 180° half-sine wave; 50 Hz;single pulse; Gate open	
V <sub>D</sub> , V <sub>R</sub>	Direct off-state and Direct reverse voltages	V	0.75·V <sub>DRM</sub> 0.75·V <sub>RRM</sub>	T <sub>j</sub> =T <sub>j</sub> max; Gate open	

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TRIGGERING				
$I_{FGM}$	Peak forward gate current	A	8	
$V_{RGM}$	Peak reverse gate voltage	V	5	$T_j = T_{j \max}$
$P_G$	Gate power dissipation	W	5	$T_j = T_{j \max}$ for DC gate current
SWITCHING				
$(di_T/dt)_{crit}$	Critical rate of rise of on-state current non-repetitive ( $f=1$ Hz)	A/ $\mu$ s	630	$T_j = T_{j \max}; V_D = 0.67 \cdot V_{DRM}; I_{TM} = 2 I_{TAV};$ Gate pulse: $I_G = 2$ A; $t_{GP} = 50 \mu$ s; $di_G/dt \geq 1$ A/ $\mu$ s
THERMAL				
$T_{stg}$	Storage temperature	°C	-60 ÷ 125	
$T_j$	Operating junction temperature	°C	-60 ÷ 125	
MECHANICAL				
F	Mounting force	kN	33.0 ÷ 40.0	
a	Acceleration	m/s <sup>2</sup>	50 100	Device unclamped Device clamped

### CHARACTERISTICS

Symbols and parameters		Units	Values	Conditions		
<b>ON-STATE</b>						
$V_{TM}$	Peak on-state voltage, max	V	1.45	$T_j = 25$ °C; $I_{TM} = 5000$ A		
$V_{T(TO)}$	On-state threshold voltage, max	V	0.85	$T_j = T_{j \max};$		
$r_T$	On-state slope resistance, max	$m\Omega$	0.120	$0.5 \pi I_{TAV} < I_T < 1.5 \pi I_{TAV}$		
$I_L$	Latching current, max	mA	1500	$T_j = 25$ °C; $V_D = 12$ V; Gate pulse: $I_G = 2$ A; $t_{GP} = 50 \mu$ s; $di_G/dt \geq 1$ A/ $\mu$ s		
$I_H$	Holding current, max	mA	300	$T_j = 25$ °C; $V_D = 12$ V; Gate open		
<b>BLOCKING</b>						
$I_{DRM}, I_{RRM}$	Repetitive peak off-state and Repetitive peak reverse currents, max	mA	200	$T_j = T_{j \max};$ $V_D = V_{DRM}; V_R = V_{RRM}$		
$(dv_D/dt)_{crit}$	Critical rate of rise of off-state voltage <sup>1)</sup> , min	V/ $\mu$ s	1000	$T_j = T_{j \max};$ $V_D = 0.67 \cdot V_{DRM};$ Gate open		
<b>TRIGGERING</b>						
$V_{GT}$	Gate trigger direct voltage, max	V	5.00 3.00 2.00	$T_j = T_{j \min}$ $T_j = 25$ °C $T_j = T_{j \max}$	$V_D = 12$ V; $I_D = 3$ A; Direct gate current	
$I_{GT}$	Gate trigger direct current, max	mA	500 300 200	$T_j = T_{j \min}$ $T_j = 25$ °C $T_j = T_{j \max}$		
$V_{GD}$	Gate non-trigger direct voltage, min	V	0.35	$T_j = T_{j \max};$ $V_D = 0.67 \cdot V_{DRM};$		
$I_{GD}$	Gate non-trigger direct current, min	mA	15.00	Direct gate current		
<b>SWITCHING</b>						
$t_{gd}$	Delay time	$\mu$ s	4.00	$T_j = 25$ °C; $V_D = 0.4 \cdot V_{DRM}; I_{TM} = I_{TAV};$ Gate pulse: $I_G = 2$ A; $t_{GP} = 50 \mu$ s; $di_G/dt \geq 1$ A/ $\mu$ s		
$t_q$	Turn-off time <sup>2)</sup> , max	$\mu$ s	250	$dv_D/dt = 50$ V/ $\mu$ s; $T_j = T_{j \max}; I_{TM} = 2000$ A; $di_R/dt = -10$ A/ $\mu$ s; $V_R = 100$ V; $V_D = 0.67 \cdot V_{DRM};$		
$Q_{rr}$	Total recovered charge, max	$\mu$ C	4000	$T_j = T_{j \max}; I_{TM} = 2000$ A;		
$t_{rr}$	Reverse recovery time, max	$\mu$ s	33	$di_R/dt = -10$ A/ $\mu$ s;		
$I_{rrM}$	Peak reverse recovery current, max	A	242	$V_R = 100$ V		

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### **THERMAL**

$R_{thjc}$	Thermal resistance, junction to case, max	$^{\circ}\text{C}/\text{W}$	0.0100	Direct current	Double side cooled
$R_{thjc-A}$			0.0220		Anode side cooled
$R_{thjc-K}$			0.0180		Cathode side cooled
$R_{thck}$	Thermal resistance, case to heatsink, max	$^{\circ}\text{C}/\text{W}$	0.0030	Direct current	

### **MECHANICAL**

w	Weight, typ	g	1000		
$D_s$	Surface creepage distance	mm (inch)	36.50 (1.437)		
$D_a$	Air strike distance	mm (inch)	16.5 (0.650)		

### **PART NUMBERING GUIDE**

DT | 70 | 2000 | 18

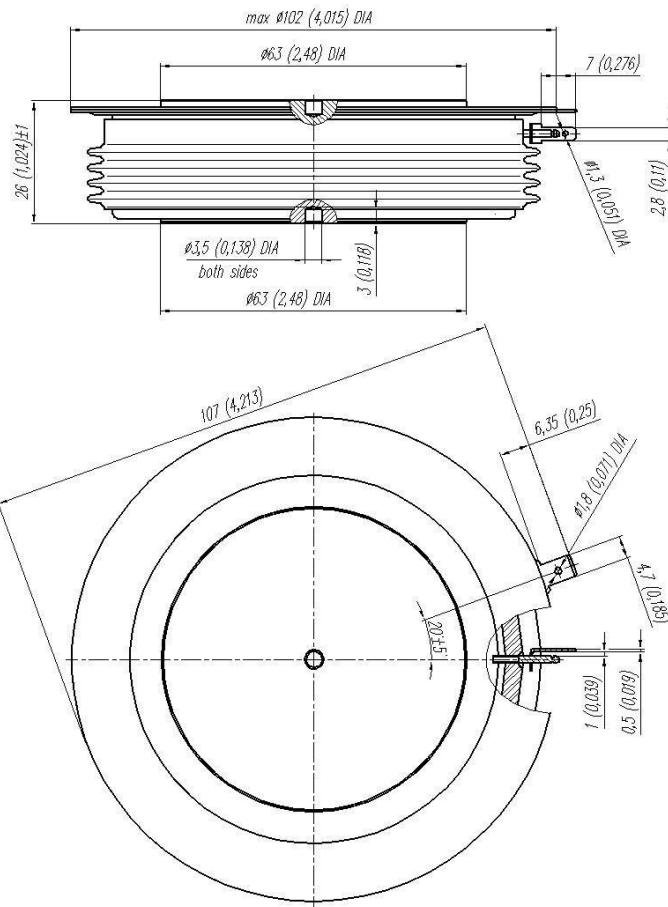
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1. DT - Phase Control Disc Thyristor
2. Element Diameter
3. Mean on-state current, A
4. Voltage code

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## **OVERALL DIMENSIONS**

## Package type: T.E3



All dimensions in millimeters (inches)